

Attorney's Docket No. 042390.P5271

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

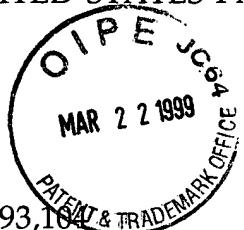
In Re Application of:

Rosenberg et al.

Application No: 08/993,104

Filed: December 18, 1997

For: VOLTAGE SIGNAL
MODULATION SCHEME



Examiner: Not Yet Assigned

Art Unit: 2871

Assistant Commissioner for Patents
Washington, DC 20231
BOX: ISSUE FEE

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Transmitted herewith is a complete set of formal drawings, comprising seven (7) sheets and seven (7) figures for the above-identified application.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 3-17, 1999

RBC

Roland B. Cortes
Reg. No. 39,152

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1026
(408) 720-8598

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

on March 17, 1999
Deborah A. McGovern

Name of Person Mailing Correspondence
Deborah A. McGovern 3-17-99

Signature Date

APPROVED BY DRAFTSMAN	O.G. FIG.	
	CLASS	SUBCLASS

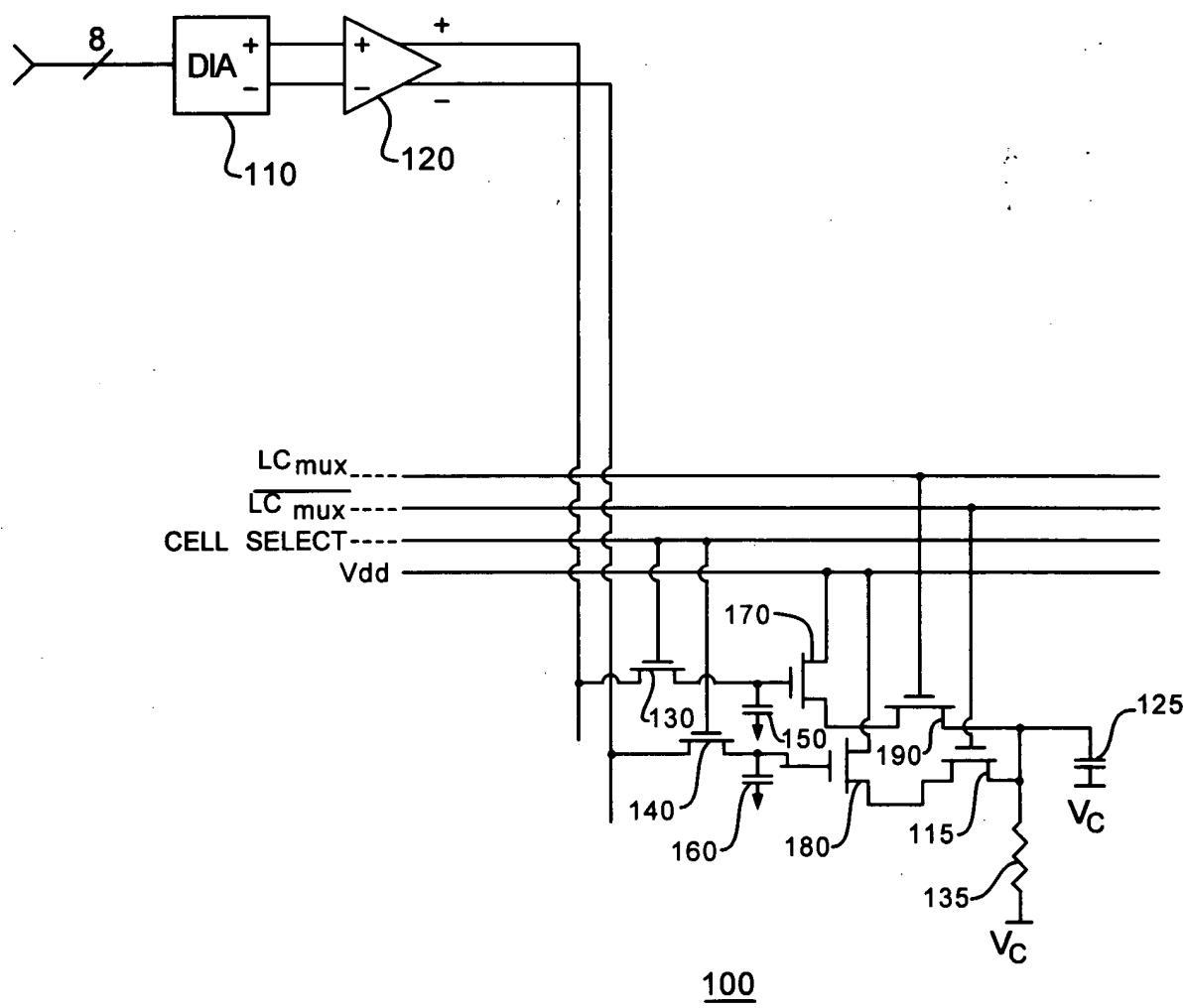


FIG. 1

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

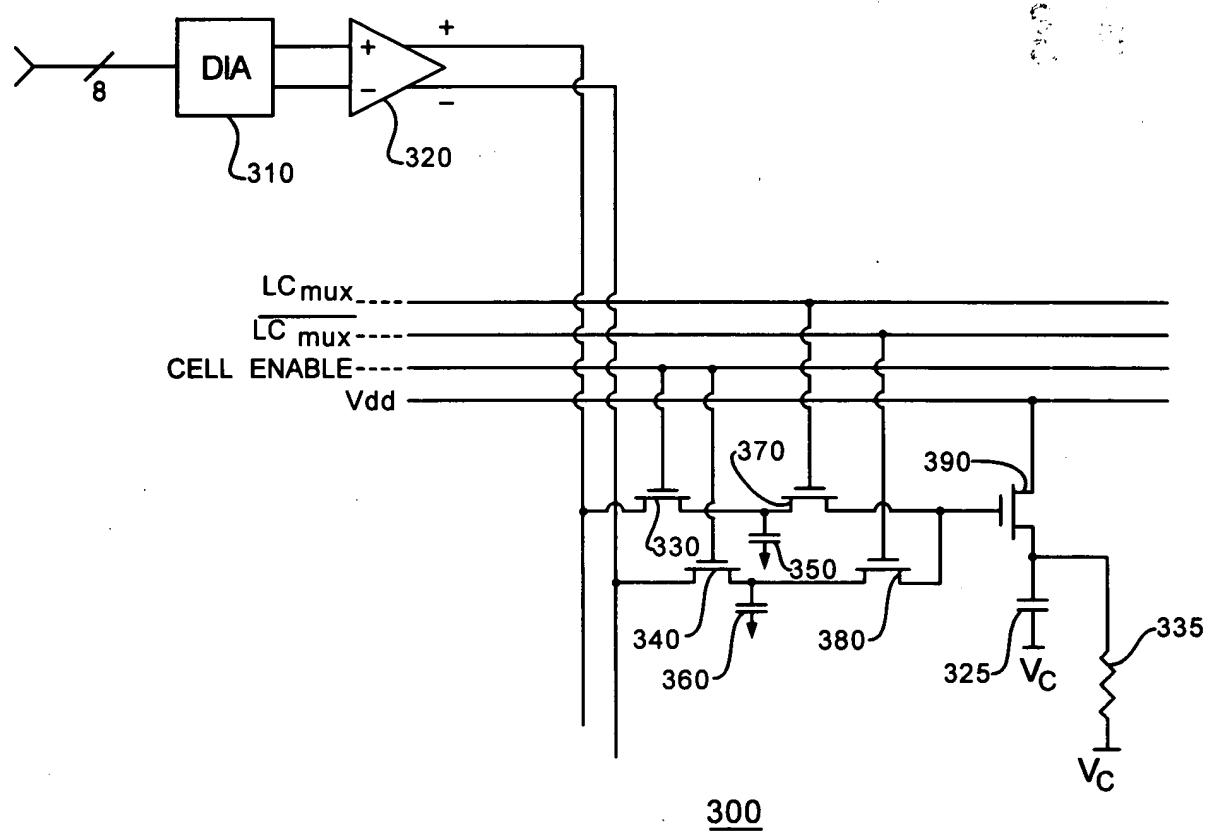


FIG. 2

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

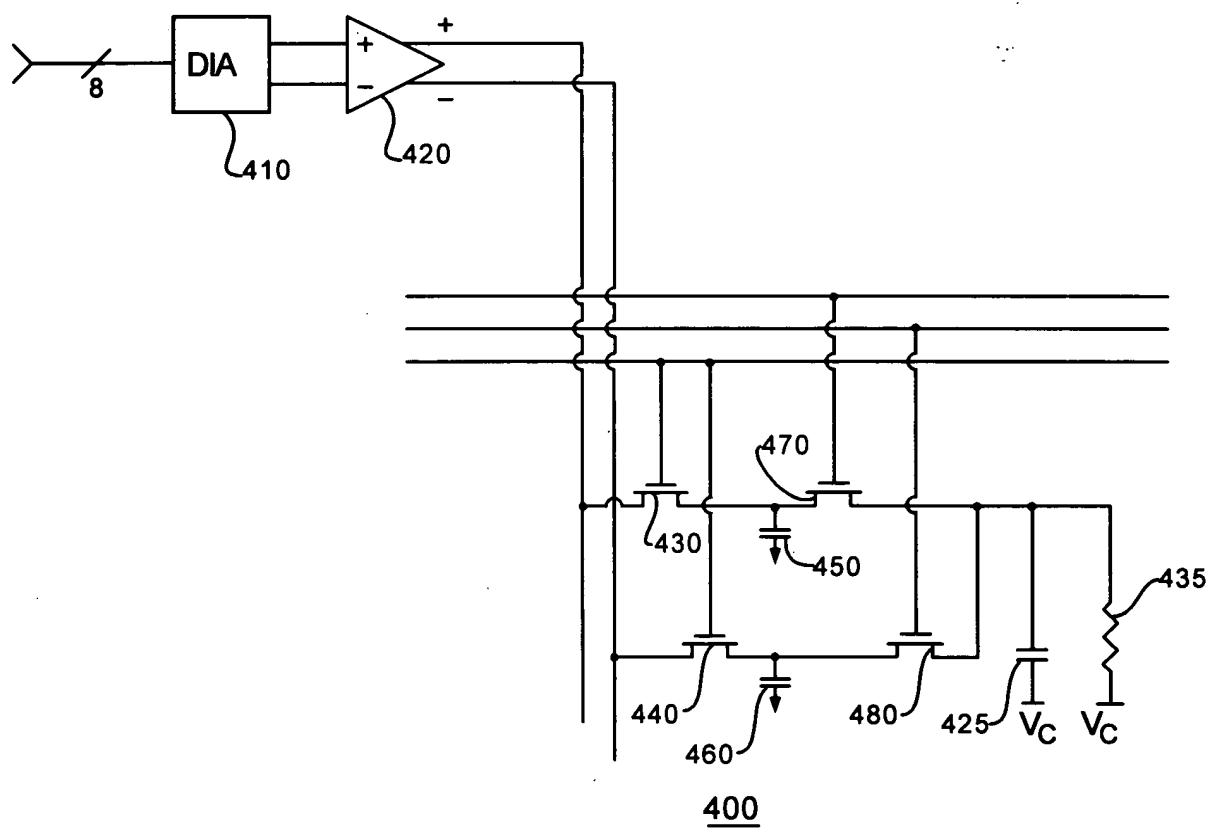
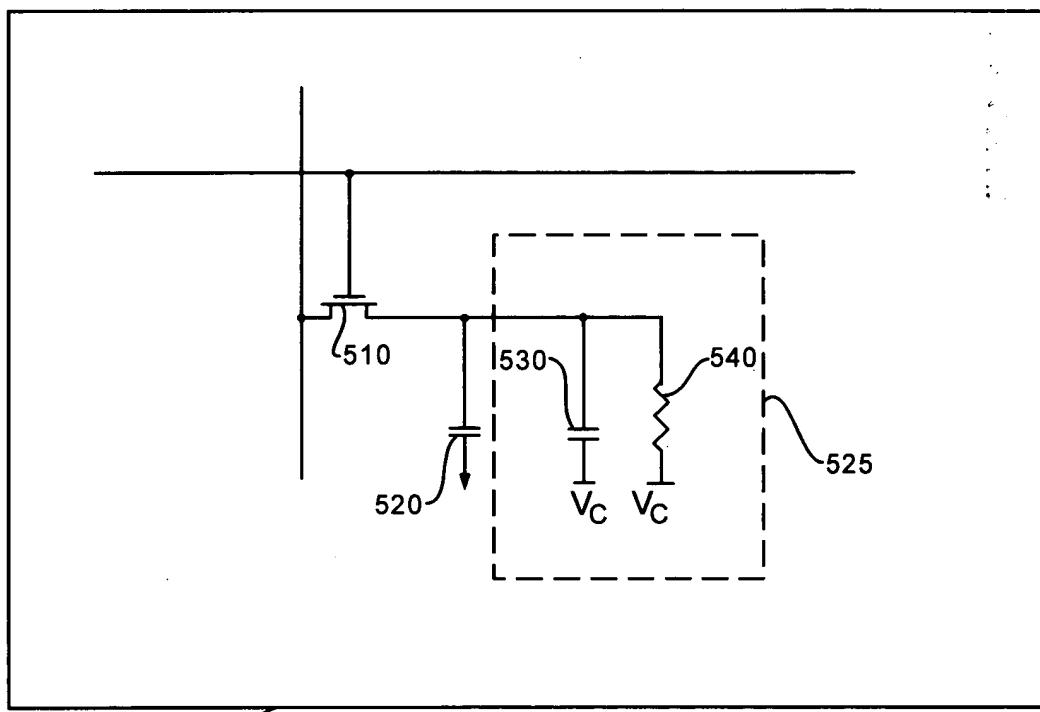


FIG. 3

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

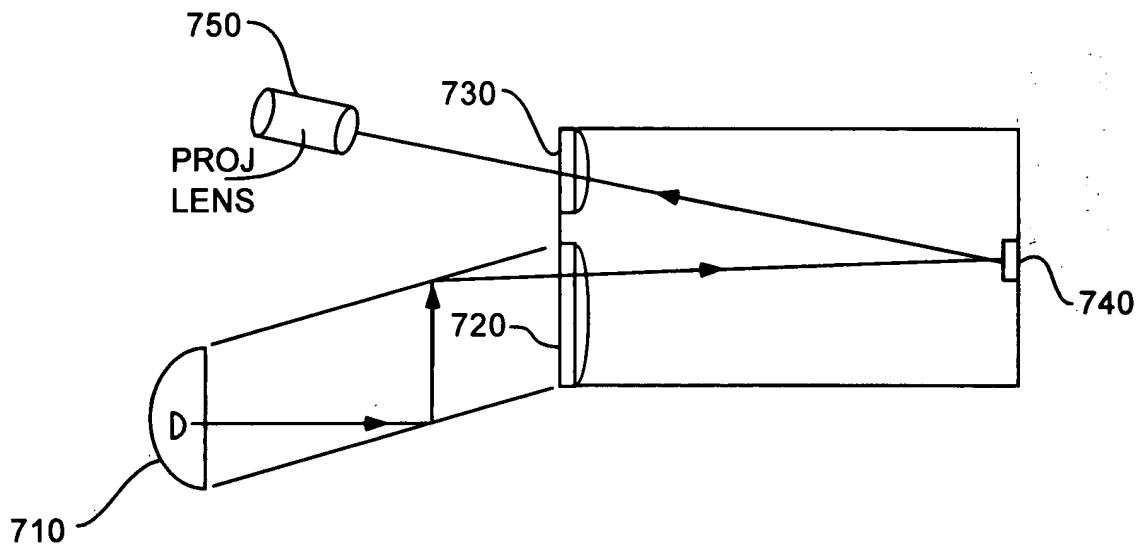


INTEGRATED
CIRCUIT (IC)
CHIP

500

FIG. 4

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		



700

FIG. 5

APPROVED BY	O.G. FIG.	
DRAFTSMAN	CLASS	SUBCLASS

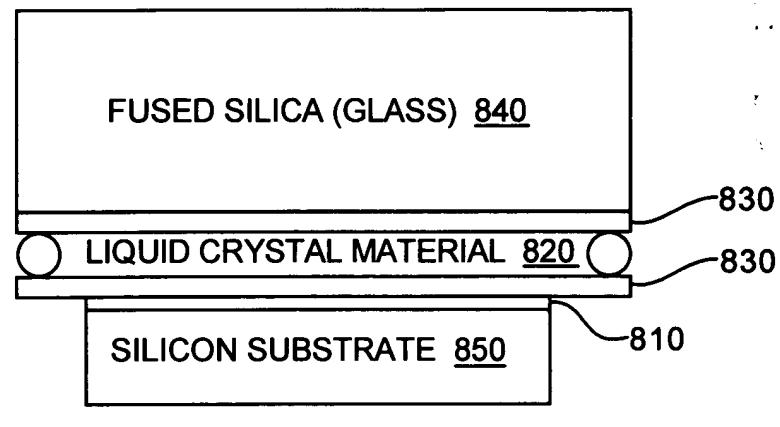
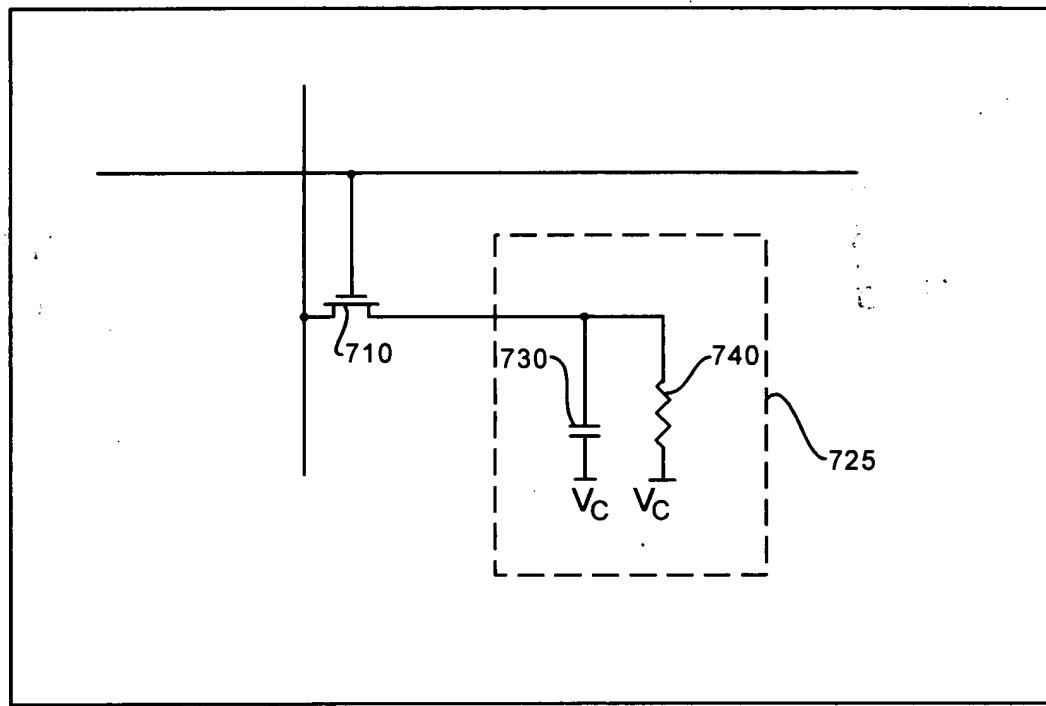


FIG. 6

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		



INTEGRATED
CIRCUIT (IC)
CHIP

700

FIG. 7